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Appl. No. 09/377,740

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DC2-482300

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/377,740
Applicant : Kimikatsu SHOJI et al.
Title : SEMICONDUCTOR INTEGRATED CIRCUIT
AND METHOD OF MANUFACTURING THE SAME

Filed : August 20, 1999
Art Unit : 2811
Examiner : S. CRANE

Docket No. : 32014-150502
Customer No. : 26694

Date : _____

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office Action dated May 20, 2003, Applicants submit the following Amendment and Reply. The Application is amended as follows:

Amendments to the Specification begin on page 2.

Amendments to the Claims are reflected in the listing of the claims which begins on page 5.

Remarks begin on page 8.

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